Abstract of the Disclosure

If a re-instruction fetch request is inputted to a box 30, a signal for holding writing in a branch history or a signal for holding a re-instruction fetch is generated. A signal for holding the re-instruction fetch, branch history information, a branch instruction address and a branch destination instruction address are inputted to the box 31. The box 31 outputs these data, and simultaneously generates a new entry in the branch history, updates an old entry or generates a signal for instructing to erase an entry. These data are inputted to the box 32, and a signal for instructing whether to write a branch instruction address, a branch destination instruction address or branch history information are generated.